

Appl. No. 10/710,673
Amdt. dated January 19, 2006
Reply to Office action of October 19, 2005

REMARKS/ARGUMENTS

1. Objection to claim 10:

Claim 10 is objected to due to informalities.

5 **Response:**

Claim 10 has been amended to correct the informalities. The recitation of "if" has been changed to --when--. Acceptance of the claims is respectfully requested.

2. Rejection of claims 1-9 and 14-17 under 35 U.S.C. 112, second paragraph:

10 Claims 1-9 and 14-17 are rejected under 35 U.S.C. 112, second paragraph as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 1 is indefinite because the recitation "...for stopping the boosting circuit from boosting the input voltage" is indefinite because it is misdescriptive.

15 Claims 6 and 12 are indefinite because the connections between the elements are confusing. The applicant is requested to show the first, second, and third resistors in the drawing and to show how they are connected to other elements.

Response:

20 Claim 1 has been amended to overcome this rejection, and the indefinite claim language has been removed from claim 1.

25 Claims 6 and 12 have been amended to show how the structure of the voltage detector is connected to the output voltage of the boosting circuit. No new matter is added through this amendment. The structure of the voltage detector is illustrated in Figure 4 of the instant application. The recited first, second, and third resistors are shown in Figure 4 as R₃, R₂, and R₁, respectively. The applicant believes that the

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limitations in claims 6 and 12 are clear in view of Figure 4.

Reconsideration of claims 1-9 and 14-17 is respectfully requested.

5 3. Rejection of claims 1 and 10 under 35 U.S.C. 102(b):

Claims 1 and 10 are rejected under 35 U.S.C. 102(b) as being unpatentable over Arakawa (US 5,436,550).

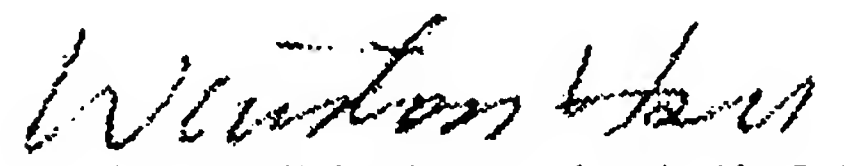
Response:

10 Claims 1 and 10 have been amended to overcome this rejection. Claims 1 and 10 now contain limitations previously found in claims 2 and 11, respectively. The claims now recite that an AND gate is used to preventing the oscillating signals produced by the oscillator from being input to the boosting circuit when the output voltage of the boosting circuit reaches the predetermined voltage. The cited prior art
15 does not teach these limitations. Claims 3-9 and 12-17 are dependent on claims 1 and 10, and should be allowed if claims 1 and 10 are allowed. Reconsideration of claims 1, 3-10, and 12-17 is respectfully requested.

20 Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

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Sincerely yours,



Date: 01/19/2006

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